



500.38017X00/E4051-02EJ

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Kazuo KATO et al.  
Appl'n No. : 09/446,507  
Filed : 27 December 1999  
For : PHASE-LOCKED LOOP CIRCUIT, INFORMATION  
PROCESSING APPARATUS AND INFORMATION  
PROCESSING SYSTEM  
Group AU : 2631  
Examiner : Tesfaldet Bocure  
Conf. No. : 2422

AMENDMENT

**Mail Stop Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

2 March 2005

Sir:

In response to the Office Action mailed 2 December 2004 in connection with the above-identified application, the following amendments and remarks are respectfully submitted.

In accordance with 37 CFR §1.121 in the Final Rule effective 30 July 2003, and as revised in the Final Rule effective 21 October 2004, each section of amendment begins on a new page, and changes are shown by strike-through (or double brackets where appropriate) and underlining to indicate deletions and additions, respectively. A complete listing of all claims ever presented in the application is given with the current status of each claim, and only the text of all pending and withdrawn claims is presented in full, with those pending/withdrawn claims not being amended herein being presented in clean version.